FORM PTO-1449	Docket Number (Optional) 100-24000 (P05764)	Application No. 10/730,658
INFORMATION DISCLOSURE CITATION IN AN APPLICATION *(Use several sheets if necessary)*	Applicant(s) James Thomas Doyle	
	Filing Date December 8, 2003	Group Art Unit ~~2800~~ 2816

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JZ	Dragan Maksimovic, Bruno Kranzen, Sandeep Dhar and Ravindra Ambatipudi, U.S. Patent Application No. 10/053,226, filed January 19, 2002, entitled "An Adaptive Voltage Scaling Digital Processing Component and Method of Operating the Same".
JZ	Bruno Kranzen and Dragan Maksimovic, U.S. Patent Application No. 10/053,227, filed January 19, 2002, entitled "Adaptive Voltage Scaling Clock Generator for Use in a Digital Processing Component and Method of Operating the Same".
JZ	Dragan Maksimovic and Sandeep Dhar, U.S. Patent Application No. 10/053,828, filed January 19, 2002, entitled "System for Adjusting a Power Supply Level of a Digital Processing Component and Method of Operating the Same".
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JZ	James T. Doyle and Dragan Maksimovic, U.S. Patent Application No. 10/160,428, filed March 26, 2002, entitled "Method and System for Minimizing Power Consumption in Mobile Devices Using Cooperative Adaptive Voltage and Threshold Scaling".
JZ	Dragan Maksimovic and James Thomas Doyle, U.S. Patent Application No. 10/166,822, filed June 10, 2002, entitled "Serial Digital Communication Superimposed on a Digital Signal Over a Single Wire".
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EXAMINER Jeffrey Zweizig	DATE CONSIDERED 10/18/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

INFORMATION DISCLOSURE CITATION

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JZ		6,498,512 B2	12/24/02	Simon et al.	326	93	02/27/01
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*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

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Applicant(s) James Thomas Doyle	
Filing Date December 8, 2003	**Group Art Unit** 2838

U.S. PATENT DOCUMENTS

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